

IN THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An image display device comprising:

a plurality of pixels which are arranged in matrix;

a plurality of data signal lines;

a plurality of scanning lines;

a first driver circuit which controls the data signal lines; [[and]]

a second driver circuit which controls the scanning lines [[,]] ; and

a testing circuit comprising an input portion, an output portion, and a portion

wherein a plurality of NAND circuits is connected in series so that an output of one of the plurality of NAND circuits is directly connected to one of input of the another one of the plurality of NAND circuits;

wherein each of the plurality of data signal lines is connected to each of input portions of the plurality of NAND circuits [[,]] ;

wherein a number of the plurality of data signal lines is equal to a number of the plurality of NAND circuits,

wherein [[an]] the output portion of the testing circuit is connected to a testing terminal and [[an]] the input portion of the testing circuit is connected to a power source, and

wherein the first driver circuit and the plurality of pixels are connected to the testing circuit through the data signal line.

2. (Original) A image display device according to claim 1, wherein an electronic device mounting the image display device is any one of a laptop personal computer, a portable information terminal, a video camera, a cellular phone, a digital camera.

3. (Currently Amended) A testing method of an image display device:

a plurality of pixels which are arranged in matrix;

a plurality of data signal lines;

a plurality of scanning lines;

a first driver circuit which controls the data signal lines; [[and]]

a second driver circuit which controls the scanning lines [[,]] ; and

a testing circuit comprising an input portion, an output portion, and a portion wherein a plurality of NAND circuits is connected in series so that an output of one of the plurality of NAND circuits is directly connected to one of input of the another one of the plurality of NAND circuits;

wherein each of the plurality of data signal lines is connected to each of input portions of the plurality of NAND circuits [[;]] ;

wherein a number of the plurality of data signal lines is equal to a number of the plurality of NAND circuits.

wherein [[an]] the output portion of the testing circuit is connected to a testing terminal and [[an]] the input portion of the testing circuit is connected to a power source,

wherein the first driver circuit and the plurality of pixels are connected to the testing circuit through the data signal line, and

wherein a testing pulse is inputted to the testing circuit and a square wave signal is supplied to an output portion of the testing terminal in accordance with the testing pulse.

4. (Currently Amended) A testing method of an image display device according to claim 3 [[:]] , wherein the testing pulse is outputted to the data signal line in accordance with the input of a video signal.

5. (Currently Amended) A testing method of an image display device according to claim 3 [[:]] , wherein the testing pulse is a High signal in all the data signal lines and is switched sequentially into a Low signal.

6. (Currently Amended) A testing method of an image display device according to claim 3 [[:]] , wherein the testing pulse is inputted simultaneously to the NAND circuits connected in series.

7. (Currently Amended) An image display device comprising:
a plurality of data signal lines;
a plurality of scanning lines extending orthogonally to said plurality of ~~gate~~ data signal lines;
a plurality of pixels surrounded by said plurality of ~~gate~~ data signal lines and said plurality of ~~source~~ scanning lines;
a first driver circuit which controls the data signal lines; [[and]]
a second driver circuit which controls the scanning lines [[:]] ; and

a testing circuit comprising an input portion, an output portion, and a portion wherein a plurality of NAND circuits is connected in series so that an output of one of the plurality of NAND circuits is directly connected to one of input of the another one of the plurality of NAND circuits;

wherein each of the plurality of data signal lines is connected to each of input portions of the plurality of NAND circuits [[;]] ,

wherein a number of the plurality of data signal lines is equal to a number of the plurality of NAND circuits.

wherein [[an]] the output portion of the testing circuit is connected to a testing terminal and [[an]] the input portion of the testing circuit is connected to a power source, and

wherein the first driver circuit is connected to the testing circuit through the data signal line.

8. (Original) A image display device according to claim 7, wherein an electronic device mounting the image display device is any one of a laptop personal computer, a portable information terminal, a video camera, a cellular phone, a digital camera.

9. (Currently Amended) A testing method of an image display device:

a plurality of data signal lines;

a plurality of scanning lines extending orthogonally to said plurality of ~~gate~~ data signal lines;

a plurality of pixels surrounded by said plurality of ~~gate~~ data signal lines and said plurality of ~~source~~ scanning lines;

a first driver circuit which controls the data signal lines; [[and]]

a second driver circuit which controls the scanning lines, and

a testing circuit comprising an input portion, an output portion, and a portion wherein a plurality of NAND circuits is connected in series so that an output of one of the plurality of NAND circuits is directly connected to one of input of the another one of the plurality of NAND circuits;

wherein each of the plurality of data signal lines is connected to each of input portions of the plurality of NAND circuits [[:]] ,

wherein a number of the plurality of data signal lines is equal to a number of the plurality of NAND circuits,

wherein [[an]] the output portion of the testing circuit is connected to a testing terminal and [[an]] the input portion of the testing circuit is connected to a power source,

wherein the first driver circuit is connected to the testing circuit through the data signal line, and

wherein a testing pulse is inputted to the testing circuit and a square wave signal is supplied to an output portion of the testing terminal in accordance with the testing pulse.

10. (Currently Amended) A testing method of an image display device according to claim 9 [[:]] , wherein the testing pulse is outputted to the data signal line in accordance with the input of a video signal.

11. (Currently Amended) A testing method of an image display device according to claim 9 [[:]] , wherein the testing pulse is a High signal in all the data signal lines and is switched sequentially into a Low signal.

12. (Currently Amended) A testing method of an image display device according to claim 9 [[:]] , wherein the testing pulse is inputted simultaneously to the NAND circuits connected in series.